Notice of References Cited

Ī	Application/Control No.	Applicant(s)/Pater	nt Under
	10/678,685	Reexamination DOUMA ET AL.	
	Examiner	Art Unit	
	LINDA WONG	2611	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-4,276,548	06-1981	Lutz, Erno B.	342/103
*	В	US-6,275,144	08-2001	Rumbaugh, Stephen Roy	375/259
*	С	US-2004/0042504	03-2004	Khoury et al.	370/518
*	D	US-5,079,770	01-1992	Scott, Paul H.	370/536
*	Е	US-2003/0195645	10-2003	Pillay et al.	700/94
	F	US-			
	G	US-		-	
	Н	US-		-	
	1	US-			
	J	US-			
	к	US-			
	L	US-			
	м	US-			

FOREIGN PATENT DOCUMENTS

FOREIGN FATENT DOCUMENTS						
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q		l			
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
*	U	Phase-Locked Loop Protocol Scheme for a Contaminated Synchronization Field. IBM Technical Disclosure Bulletin, May 1990, US Vol. No. 32, Issue 12, No. 336-337 Publication Date: May 1, 1990 (19900501)				
*	v	Electronics Tutorials: Transistors, November 15, 2000, http://web.archive.org/web/20001203135600/ http://www.electronics-tutorials.com/basics/transistors.htm				
*	w					
	х					

"A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.